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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	10/807,022
				Filing Date	March 23, 2004
				First Named Inventor	WALK, Michael J.
				Group Art Unit	2815
				Examiner Name	Clark, Jasmine Jhihan B.
Sheet	2	of	2	Attorney Docket Number	P18288

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
gbe	G	"Solderless Assembly Method for Semiconductor Carriers", IBM Technical Disclosure Bulletin, IBM Corp. New York, US, Vol. 35, No. 3, 1 August 1992 (1992-08-01), pages 130-131, XP000326203, ISSN: 0018-8689.	
gbe		"PCT Notification of Transmittal of the International Search Report", mailed 27/07/2005, for PCT/US2005/008604	

Examiner Signature	Jasmine J Clark	Date Considered	12/05/05
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